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January 9, 2007

TO: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Art Unit 2811 - Examiner Arora, Ajay

FROM: Stephen B. Ackerman, Reg. No. 37,761
28 Davis Avenue
Poughkeepsie, N.Y. 12603

SUBJECT: Serial #: 10/802,566
File Date: 03/17/2004
Inventor: Mou-Shiung Lin
Examiner: Arora, Ajay
Art Unit: 2811
Title: A High Performance IC Chip having Discrete Decoupling
Capacitors Attached to its IC Surface

RESPONSE TO NON-FINAL OFFICE ACTION

Dear Sir:

In response to the Office Action mailed Oct. 10, 2006, please amend the above-identified application for patent as follows:

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents P.O. Box 1450, Alexandria, VA 22313-1450 on Jan. 9, 2007.

Signature 
Stephen B. Ackerman, Reg. No. 37,761

Date: Jan. 9, 2007

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the listing of the Claims which begins on page 6 of this paper.

Amendments to the Drawings begin on page 22 of this paper and include an attached replacement sheet.

Remarks/Arguments begin on page 23 of this paper.

An Appendix including amended drawing figures is attached following page 34 of this paper.